FIG. 1

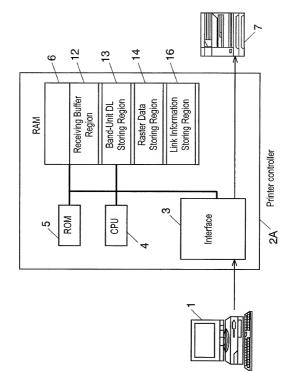


FIG. 2

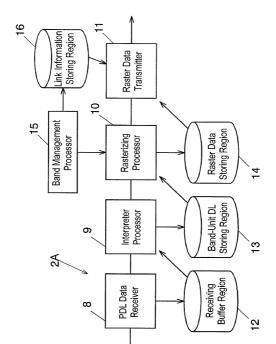
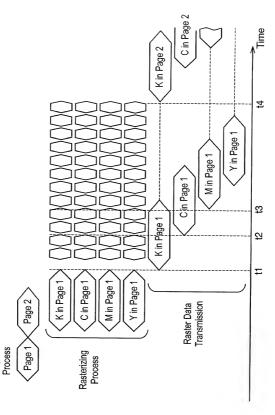


FIG. 3

Interpreter



C-2(1)

M-2(1)

K-2(2)

C-2(2)

C-2(2)

M-2(2)

K-2(4)

M-2(4)

M-2(4) FIG. 4B Ē000, \$\$\$\$\$\$\$\$\$\$\$\$ Top Address Time t3 7 FIG. 4A Time t2

FIG. 5

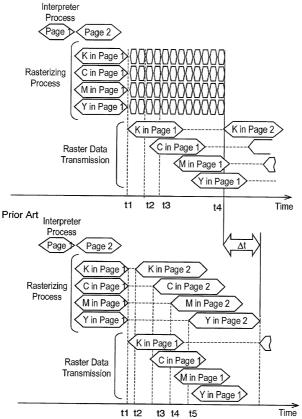


FIG. 6

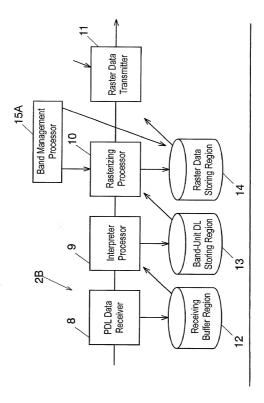


FIG. 7

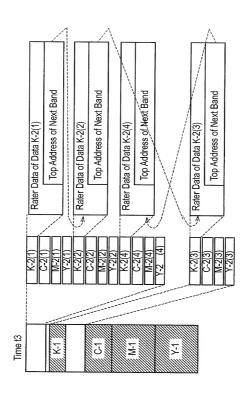
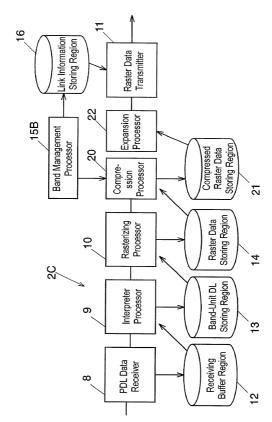


FIG. 8



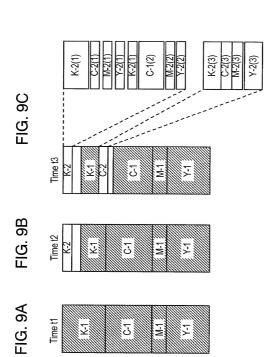


FIG. 10

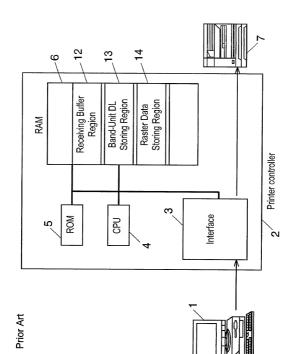


FIG. 11

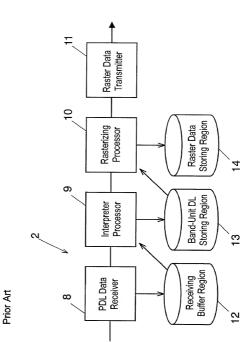
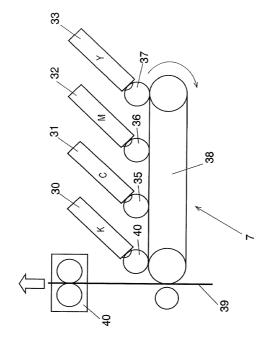
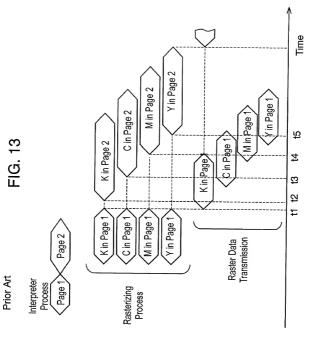


FIG. 12





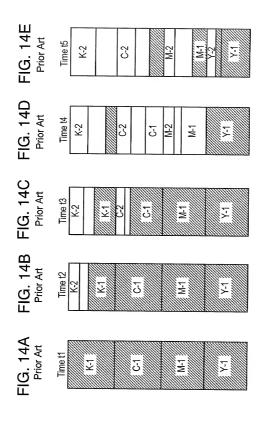


FIG. 15

